

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1874	710/22-28.ccls. or 709/212.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 08:58
S2	182	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context) near3 (register buffer)) with (count\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 15:42
S3	184	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer)) with (count\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 09:10
S4	11	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer)) with (count\$3) with (next near3 (register buffer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 13:21
S5	16	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (next near3 (register buffer pointer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 15:45
S6	0	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (lookahead near3 (register buffer pointer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 09:18
S7	0	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (look-ahead near3 (register buffer pointer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 09:17
S8	0	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with ((look adj ahead) near3 (register buffer pointer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 09:17

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S9	0	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (lookahead))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 09:18
S10	23	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with ((next link\$4 chain\$4) near3 (register buffer pointer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 09:30
S11	10	S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with ((link\$4 chain\$4) near3 (register buffer pointer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 10:15
S12	47	S1 and (DMA with (prefetch read?ahead look?ahead))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 10:16
S13	584	(DMA with ((setting operation control\$4 tranfer descriptor context) near3 (register buffer)) with (count\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 15:42
S14	17	(DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer)) with (count\$3) with (next near3 (register buffer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 15:43
S15	23	(DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (next near3 (register buffer pointer)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 15:45
S16	720	(DMA with (select\$4) with (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:17
S17	135	(DMA near3 (select\$4) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:22

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S18	3	(DMA near3 (select\$4) near3 (register)) with (external near3 memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:24
S19	21	(DMA near3 (select\$4) near3 (register)) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:26
S20	24	(DMA near3 (select\$4 mux multiplex\$4) near3 (register)) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:27
S21	332	(DMA with (select\$4 mux multiplex\$4) with (register)) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:27
S22	123	(DMA with (select\$4 mux multiplex\$4) near3 (register)) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:27
S23	51	(DMA with (select\$4 mux multiplex\$4) with (register)) with (external near3 memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 10:28
S24	21	(DMA with (register) with (transfer near3 condition))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 13:38
S25	6	(DMA with (register) with (transfer near3 condition)) with (memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 13:31
S26	31	(DMA with (external near3 memory) with (setting))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 13:42
S27	718	(DMA with (memory) with (setting))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 13:42

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S28	110	(DMA near3 (memory) near3 (setting))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:00
S29	9	(DMA near3 (memory) near3 (setting) near3 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 13:49
S30	14	(DMA near3 (memory) near3 (parameter setting) near3 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 13:51
S31	12	(DMA near3 (memory) near3 (instruction) near3 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 13:51
S32	167	(DMA with (instruction near3 (memory)) with (transfer))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:08
S33	20	(DMA with (instruction near3 (memory)) with (transfer)) with CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 14:08
S34	45	(DMA with (internal near3 control near3 register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 09:32
S35	347447	((DMA near3 (controller engine)) or (memory near3 access))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 13:22
S36	999	((DMA near3 (controller engine)) or (memory near3 access)) with (register) with (next subsequent)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 13:23
S37	64	((DMA near3 (controller engine)) or (memory near3 access)) with (register) with ((next subsequent) near3 (transfer\$4 transmission))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 13:23